# *Defensible Aliasing and Contrast Divisional Err(Or) Sub-Classification Unto Determination of Bit-Wise Extremal Fore-Notice of Decimation of Alternatively Provided Limitation*

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1.)       To what is provided of 'an'(d) (each;), one (1)/(-0) subclassification of what is randomized or non-randomized err to type of class subsidiary MCU relation entitles the pre-contextual provision of lossless encoding and (&&) transcription to void and null co-determination of semi-stochastic Markov and Pascal or . and subclassification of machine type code by pre-limitation of Gibb(s)' phenome[non](a) through the principle of separation of variance and a-modal summation of a limited separation via binomial standard deviation under one relation of secondarily provided mean equidistant summation under de-limitation of each subsidiary threshold and stocasticity of subsidiary contextual layered equipartition by the Fundamental Theorem of Algebra.

2.)       When one equidistant measure is equated by that of secondary standard deviation; as error bar's grow in apportion with(in) a given thresh-hold function; the limitation of one point like dis-locative inflection point of standard equipartition outweighs in algebriac mean sum the divisional 'err'(or) of one alternatively provided congruence of set theoretic union by that of emptiness of dis-locative tertiary givenly provided variance of any said auxiliary variable - to which variance is in non-err(or)' of standard subsidiary deviance; and the equiability of two and a third of either rational or irrational exponentiated summation under a Jacobi Field of non-redressable variable bit-wise exterior product under reduction to two factual statements of any one! adaptive algorithmic structural deficit of domain wall or pre-functory re-attrition of further(ed) deficit of non-declarative typing and global variable to what is 'top' of stack and core of MCU.

3.)       Two adaptive dis-locative re-attritional preceded limitations to Non-Polynomial and Polynomial equipartition of summation are therefore resolvable by any micro-minature or non-stated element of adaptive algorithm by freely held associate in surpassing a given domain of two established un-free notions; that of locative admixture of frequency subsidiary and open evanescent wave subsidiary structure under retraction of Bessel Series; and trans-positional Lagrange Multiplier by free differentiation of locability of zero (0)-(1) pre-advanced subsidiary machine typing code in (with) variance of entitled obligation to MCU kernel via Science Lab Anaconda; of which is it's free domain of runoff and buffer stack over-fill de-limitation prior any given advanced bit-wise extremal dis-locative identifier or locability of kernel swap and parallel processing of admixture free delimitation of pro-gressive and re-gressive automatic return of one exception; to which any two alternative clock run over-fill translocative domains are freely swappable in hardware or software emulation or established type-facing of machine code run.

4.)       When the aprobilistic limitation of but one locable zero is identified a re-tractile and (manual) action of release-to-catch under pre-enabled release to open defaceable externalized non-nome relation must be excised to that of limitation of machine typing code structure variant; to then the said given difference of identifier and cross-identifier in conventional technology for which any two alternatively provided err-(or)/(&) recurrence(s) under one dis-entitled connotative and inflective retraction of binary bit-wise extremem proceeds to a halt; advantaging that of alternative option of redressability of indicat(or)y nature to a branch function; entitling that of re-labeling of connotative structural deficit of pre-catagorization of machine deficit to false err(or) recurrence default over-run; buffer-stack over-flow; pre-terminal exceptionable factually known identification; (&&) lossless deficit to re-apportionment of given provision of any three or two alternatively provided machine implementations under cross-linked dichotomy of tailored method subclass to MCU under transmission and reception via conventional communications.

5.)       That of err(or)'s under entitled identification and machine re-coupment are within a deficit of err for e(x)clusive 'or' & (n[o]t and), for then in the given provision of yet a pre-advantage look-up of typed variant to subclassification of entitled machine code over-flow of but yet one stack; that of deficit of error of furtherance is afforded by that of machine limitation of one pre-contextual type to sub-classification of known for two attritional defaults; for with(in) the above connotative sub-classifications of machine limitation the provision is unto an exceptionable default when it is freely provided that ir-rational vectorial spline re-consturcts that of variance of mean of 'rational' quotient of difference of any two bit to nibble relations of machine type.